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You acquire the image using the CPU and then send it to the FPGA via direct memory access (DMA) so the FPGA can perform operations such as filtering or color plane extraction. Then you can send the image back to the CPU for more advanced operations such as optical character recognition (OCR) or pattern matching.

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same key, or for board testing, configure
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FPGA-based implementations of MD5,
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A field-programmable gate array (FPGA) is an integrated circuit designed to be configured by a customer or a designer after manufacturing – hence the term "field-programmable". The FPGA configuration is generally specified using a hardware description language (HDL), similar to that used for an application-specific integrated

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circuit (ASIC). Circuit diagrams were previously used to specify ...

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This is another great application for FPGA based image processing, the ability to work with the sensors for long integration times and process and store the image is a real

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benefit. Of course in a real system, you might even stop the majority of the FPGA clocks while the image is being captured to prevent the additional noise from being injected into the captured image.

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